

EVPLC : Engineered EV Powerline Communication

The ultra-high-performance module for critical EV-to-charger communication. Optimized for reliability in mission-critical charging infrastructure.

[DATASHEET](#)

COMPLIANCE & INTEGRATION

Standardized for Global Interoperability

ISO 15118



DIN SPEC 70121



IEC 61851-1



SAE J1772



The EVPLC is a high-performance Powerline Communication (PLC) module designed for mission-critical synchronization between electric vehicles and CCS charging infrastructure. Built on a powerful processor and next-generation PLC chipset, the module enables secure, high-integrity data flow.



Optimized for low noise operation and high signal integrity, ensuring stable data exchange even in high-interference environments.



HIGH PERFORMANCE

Powered by the AM62x processor and QCA7006AQ PLC chipset, delivering fast and reliable EV-to-charger communication.



STANDARDS-COMPLIANT

Supports ISO 15118 and DIN SPEC 70121 ensuring compatibility with modern EV charging infrastructure.



FLEXIBLE INTEGRATION

Supports multiple charger configurations with DIP-switch CAN ID configuration and isolated design.



RELIABLE DESIGN

Advanced PLC coupling and low-noise architecture ensure stable communication in high-power environments.

Engineered for Precision

**AM62x Processor**

High-Performance processor for fast communication processing and system control.

**QCA7006AQ Chipset**

Next-Generation PLC Chipset optimized for EV charging communication.

**Automatic Protocol Switching**

Seamless switching between ISO 15118 and DIN SPEC 70121 charging protocols.

**Multiple Gun Support**

Support for Multiple Gun Configurations with DIP switch CAN ID selection.

**Isolated Hardware**

Isolated Hardware Design for improved system reliability and protection.

**Ultra-Low Noise Floor**

Implementation for stable PLC communication even in challenging environments.

**Optimized Line Coupling**

Maximum signal-to-noise ratio (SNR) for robust data transmission.

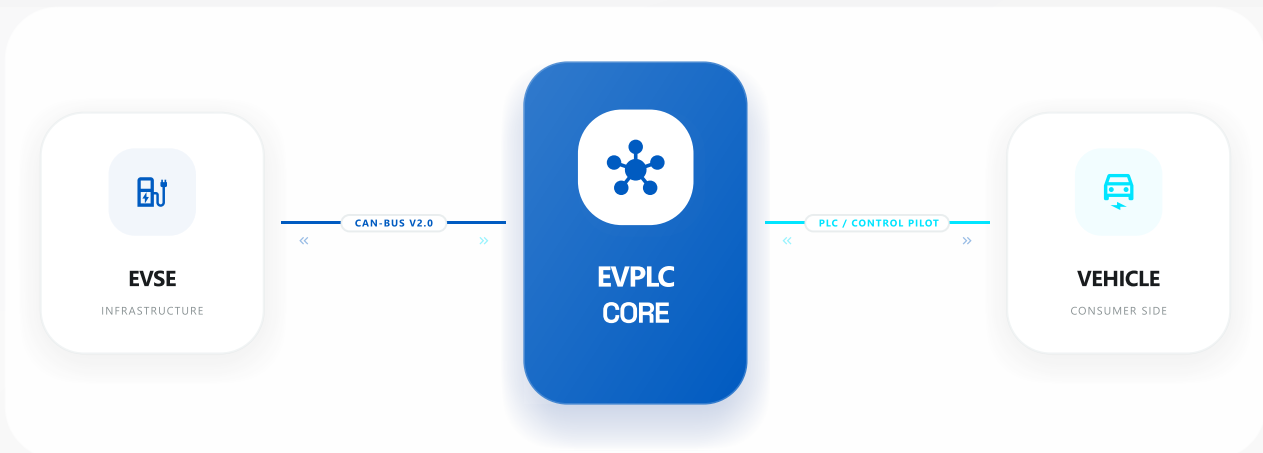
**Control Pilot Interface**

IEC 61851-1 / SAE J1772 compliant PWM generation and detection.

SIGNAL FLOW

Communication Gateway

Integrated architecture linking infrastructure protocols to vehicle-side systems with absolute precision.



RELIABILITY & LONGEVITY

PRODUCT WARRANTY

5+ Years*

OPERATIONAL LIFESPAN

15+ Years*

*Terms and conditions apply based on operating environment, usage patterns, and proper installation protocols.

Technical Specs

MAIN CPU	Texas Instruments AM62x
PLC CHIPSET	Qualcomm QCA7006AQ
COMMUNICATION	CANbus FD/2.0
CANBUS ID	Configurable via DIP
DIMENSIONS	15 x 11 x 3 cm
DEBUGGING	UART Interface
LED INDICATORS	3.3V, 5V, PLC, CANbus
VOLTAGE SUPPLY	8V to 15V DC
SAFETY	HV Galvanic Isolation



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